

Docket No.: P2002,0598

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : JOERG VOLLRATH ET AL.
Filed : CONCURRENTLY HEREWITH
Title : CIRCUIT CONFIGURATION FOR THE BIT-PARALLEL
OUTPUTTING OF A DATA WORD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Patent Abstracts of Japan 10308096 A (Churuki), dated November 17, 1998;

Patent Abstracts of Japan 10116491 A (Kazuyoshi), dated May 6, 1998.

Respectfully submitted,


For Applicants

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Date: July 15, 2003

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FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: P2002,0598
Appl. No.:

Applicant: JOERG VOLLRATH ET AL.

Filing Date: July 15, 2003
Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J	10308096 A	11/17/98	Japan				
	K	10116491 A	5/6/98	Japan				
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER	DATE CONSIDERED	